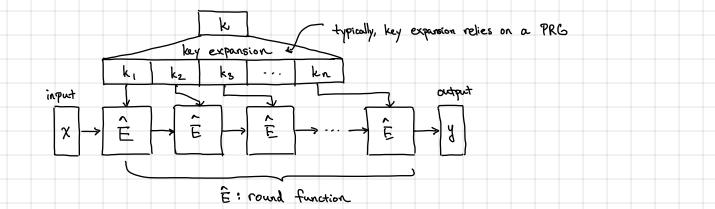
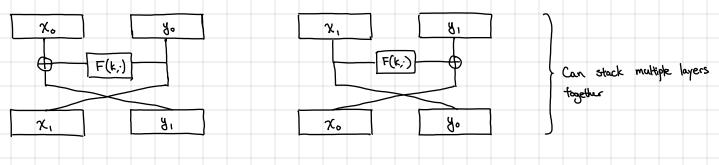
Constructing black ciphers: typically, relies on an "iterated cipher"



Difficult to design! Never invent your own crypto - use well-studied, standardized constructions and implementations! We will look at two classic designs:

on modern Intel processors, (with AES-NI), my cycles round - DES/3DES (Data Encryption Standard) 1977 (developed at IBM) - AES (Advanced Encryption Standard) 2002 [most widely used block cipher, implemented in hardware in Intel processors]

DES: relies on the Feistel design:



Evaluation

Inversion

Observe: the function F does not have to be invertible => Feistel returner is still invertible!

Theorem (Luby-Rackoff). If F is a secure PRF, then a 3-round Feistel construction yields a secure PRP. Similarly, a 4-round Feistel construction implements a strong PRP.

> I a PRP where the adversary can also query the inversion oracle (i.e., F"(k,.)

 \longrightarrow Shows that Feistel construction is sound for constructing block cipler in the pseudorandom world and f-'(.) (but now reed a good random-looking function F) in the random world) > called the round function > DES round function will not be a PRF, so

DES: block size: 64 bits -> round function operates on 32-bit blocks overall construction relies on more rounds key size: 56 bits (to comply with export control regulations) (but general design philosophy supported by theory)

used to derive 16 round keys (48 bits)

¹ DES overall is a 16-round Feistel network

-> simple approach: each 48-bit key is subset of the original 56-bit key

DES round function F(k, x):

48-bit key 32-bit x expansion function (rearrange + replicate input) 48-bit 48-bit 48-bit value S_1 S_2 S_3 S_4 S_5 S_7 S_8 S-boxes (subatitution boxes) each S-box maps 6-bits to 4-bits (carefully designed to be non-linear) P permutation of the output implemented as a truth table (hard-wined in the DES specification) only source of non-linearity in the design 32-bit y S-box design extremely important for security NSA knews of these techniques in the later 70s! L> NSA made recommendation to tweak some entries L> disclosed in 1994 after discovery of <u>differential cryptunalysis</u> that Stokes were designed to be robust against these attacks 56-bit keys was a compromise between 40-bit keys (NIST/NSA) and 64-bit keys (cryptographers-notably Hellinen) L> turned out to be insufficient - 1997: DES challenge solved in 96 days (massive distributed effort) - 1998: with dedicated hardware, DES can be broken in just 56 hours -> not secure enough! - 2007: Using off-the-shalf FPGAs (120), can break DES in just Q.8 days - anyone can now break DES! L> 2-DES: apply DES twice (keys now 112-bits) has meet-in-the-middle attack gives no advantage (though space usage is high) → 3-DES: apply DES three times [30E3((k,,k,k,),×) := DES(k3, DES(k2, DES(k,,×)))] 1-> 168-bit keys - Standardized in 1998 after brute force attacks on DES shown to be feasible AES (2002 - Most common block cipher in use today): - 3DES is slow (3x slower than DES) - 64-bit block size not ideal (recall that block size determines adversary's advantage when block eightr used for encryption) AES block cipher has 128-bit blocks (and 128-bit keys) (but block size always 2128) Lo follows another classic design paradigm: interanted Even-Mansour (also called alternating key ciphers) Even-Mansour block cipher: keys (k,, kz), input X: Theorem (Even-Mansour): If This modeled as a random permutation, then the Even-Mansour block cipher is secure (i.e., it is

a secure PRP).

The AES block cipher can be viewed as an iterated Even-Mansour cipher: key-size 128-bit key AES key expansion (key schedule) AES-128: 10 rounds AES-192: 12 rounds AES-256:14 rounds (block-size all 128 bits) Permutations TLAES and TLAES are fixed permutations and <u>cannot</u> be ideal permutations > cannot write down random permutation over > Cannot appeal to security of Even-Mansour for security {0,13128 L> Buck still provides evidence that this design strategy is viable [similar to DES and Luby-Rachoff] AES round permutation: composed of three invertible operations that each operate on a 128-bit block 0,0 0, 0, 0, 03 SubBytes: apply a fixed permutation $S: \{0,1\}^8 \rightarrow \{0,1\}^8$ to each cell hard coded in the AES standard (similar to S-box) ay as as ar a8 a9 a10 a11 (chosen very carefully to resist attacks) Q2 Q13 Q14 Q15 ShiftRows: cyclic shift the rows of the matrix - 1st row unchanged (\mathbb{F}_2) 128 bits arranged - 2nd row shifted left by I elements are polynomials over GF(2) modulo the irreducible polynomial 78 + 24 + 23 + 2+1 in 4-by-4 grid of - 3rd now shifted left by 2 bytes (80,138) - 4th now shifted left by 3 MixColumns: the matrix is interpreted as a 4-by-4 matrix over GF(28) and multiplied by a fixed invertible matrix (also carefully chosen and hard-coded into the standard) Observe: Every operation is invertible, so composition is also invertible TTRES : SubBytes; ShiftRows; MixColumns TTAES: SubBytes; ShiftRows No MixColumas for the last round done so AES decryption circuit batter L resembles AES encryption Security of AES: Brute-force attack: 2128 Best-known key recovery attack: 2^{126.1} time — only 4x better than brute force! What does 2¹²⁸ - time look like? - Suppose we can try 2⁴⁰ keys a second. \rightarrow 2⁸⁸ seconds to break 1 AES key ~ 10¹⁹ years (710 million times larger than age of the universe!) Total computing power on Earth (circa 2015) La estimated to be ~20 operations/second (currently, bitcoin mining computes ~ 26 hashes/second) Let's say we can do 2 operations / second L> still require 2 seconds to break AES ~ 9 million years of compute If we move to 256-bit keys, best brute force attack takes 22542 time (on AES-256) e.g., quantum Computers In well-implemented systems, the cryptography is not the weak point - breaking the crypto requires new algorithmic techniques > But side channels / bad implementations can compromise crypto